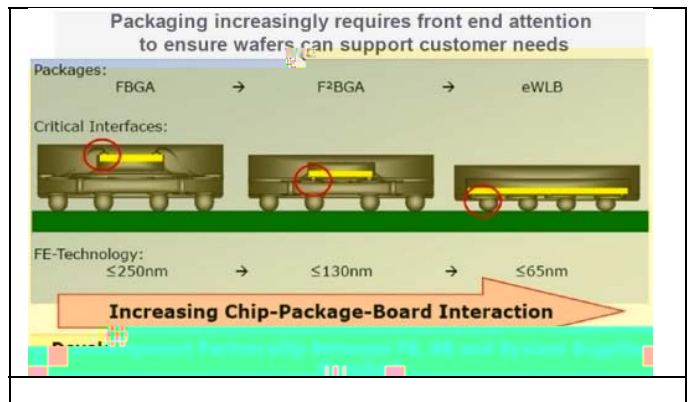


Kang Chen, Linda Chua, Won Kyung Choi, Seng Guan Chow and Seung Wook Yoon*

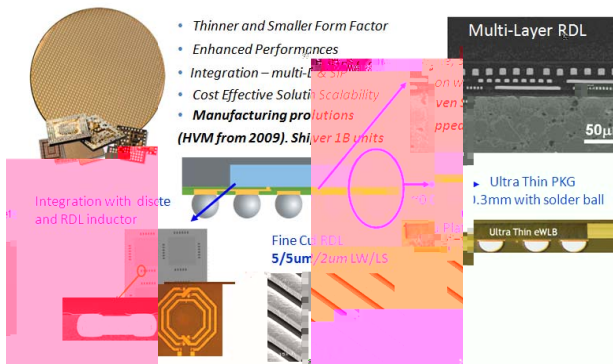
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Abstract

I.



Advanced Wafer-Level Technology: eWLB/FOWLP

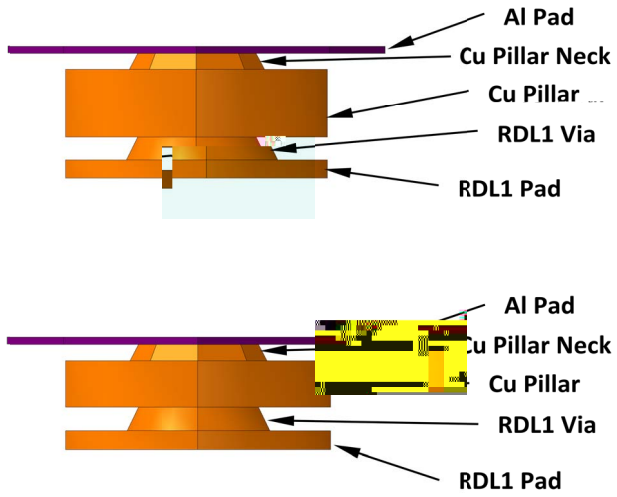


II.

Test Vehicle Specification

Reliability Test

Via Shape



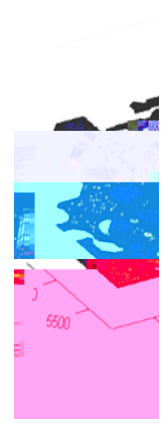
0.05mm

Temperature (°C)	Design Option			
	1	2	3	4
250	1.00	0.91	0.87	0.79
125	1.00	0.93	1.15	1.20
25	1.00	0.88	1.04	1.09
-55	1.00	0.92	1.03	1.08

Electrical S

Thermal Characterization

1. High-match
2. Trace
3. Trace and 5
4. eWLI talk i



Net	Inc
	flipchip
1	1.77
2	2.03
3	1.51
4	1.08

